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Attorney's Docket No.: 07977-175002 / US3397

1 2815 \$

#5/Ex  
3/27/12  
Surler

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2815  
Serial No.: 09/736,724 Examiner : Paul E. Brock  
Filed : December 13, 2000  
Title : INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD OF  
MANUFACTURING THE SAME

Commissioner for Patents  
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.  
The required fees are computed below. Please apply any charges  
not covered, or any credits, to Deposit Account No. 06-1050.

Total						
Claims	73	-	67	=	6	\$108
Independent	16	-	16	=	0	\$0

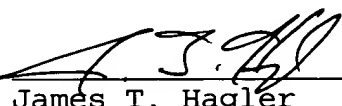
Applicant hereby petitions under 37 C.F.R.  
\$1.136 for a 2 month extension of time. \$400

TOTAL FEE DUE \$508

A check for \$508 is attached.

Respectfully submitted,

Date: 12/28/01

  
James T. Hagler  
Reg. No. 40,631

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL  
I hereby certify under 37 CFR \$1.8(a) that this  
correspondence is being deposited with the  
United States Postal Service as first class mail  
with sufficient postage on the date indicated  
below and is addressed to the Commissioner for  
Patents, Washington, D.C. 20231.

Date of Deposit 12/28/2001

Signature Deborah K. Sim

Typed or Printed Name of Person Signing  
Certificate Deborah K. Sim